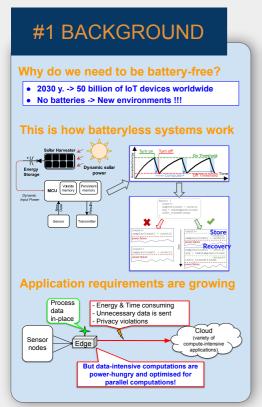
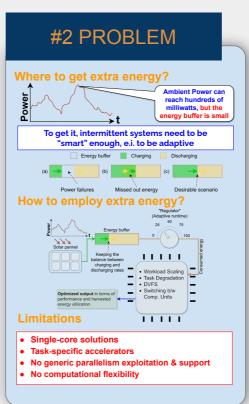
## **AdaMICA: Adaptive Multicore Intermittent Computing**

Khakim Akhunov, Kasim Sinan Yildirim



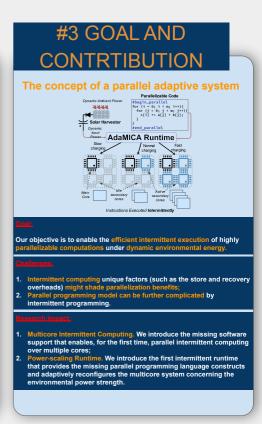


**Programming model** 

or (i = start; i < end; i++){
for (j = 0; j < m; j++){
 for (k = 0; k < m; k++){
 c[i][j] += a[i][k] \* b[k][j];

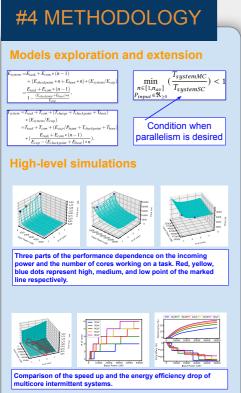
void adamica(fn\_ptr \*func) {

decide on the number of o Cores = DMT(func, power);



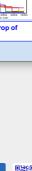
 $W_L * P_{current} + P_{past}$ 

 $W_L + 1$ 

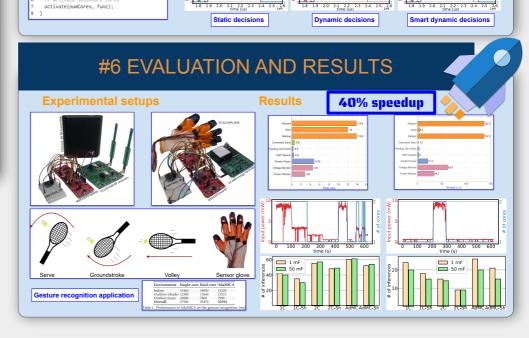


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#5 ADAMICA ARCHITECTURE

**Operating overview** 

**Decision-making efficiency**