Runtime Adaptive Hardware/Software Execution in Complex Heterogeneous Systems

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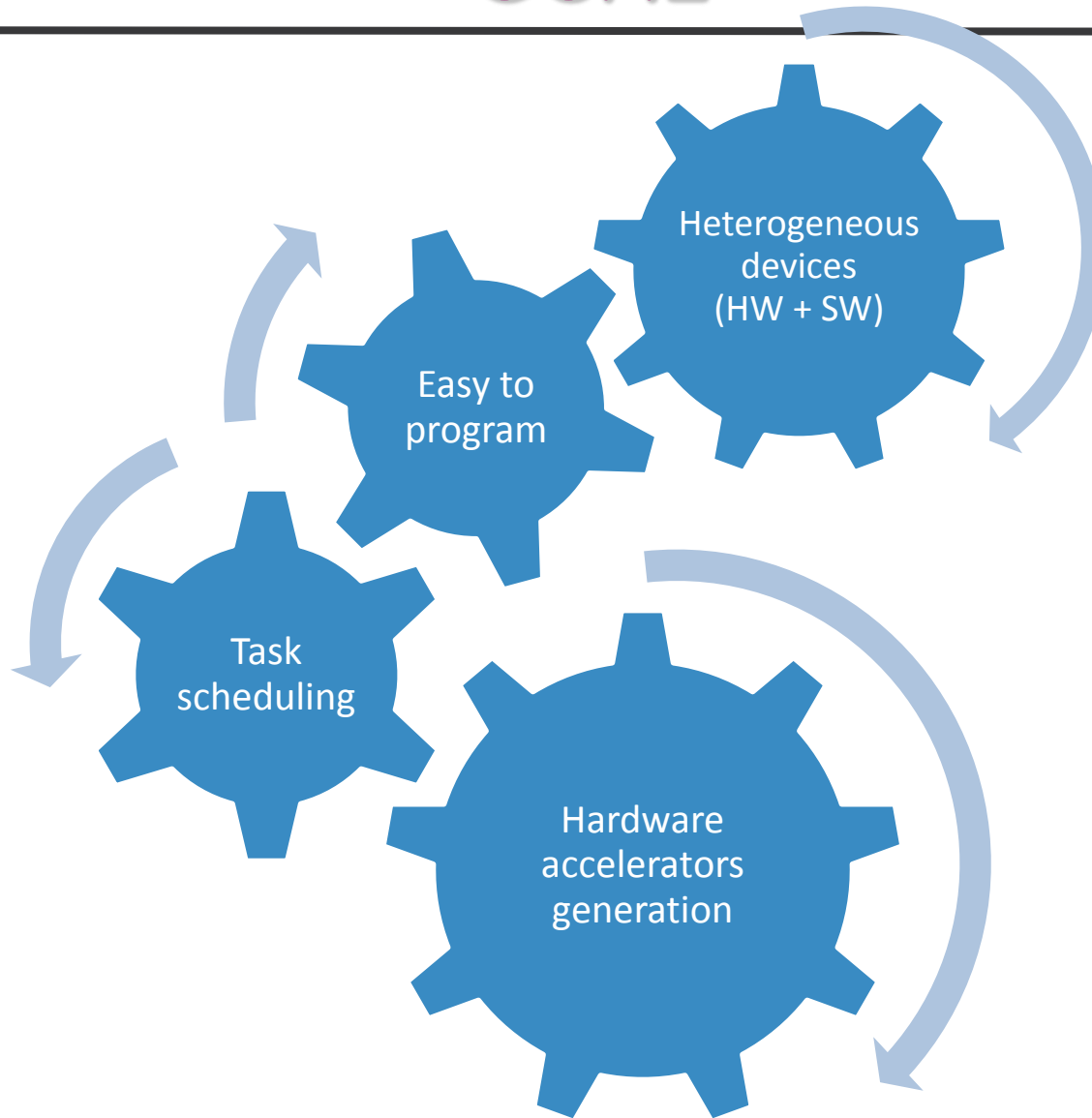cei@upm.es

Timetable

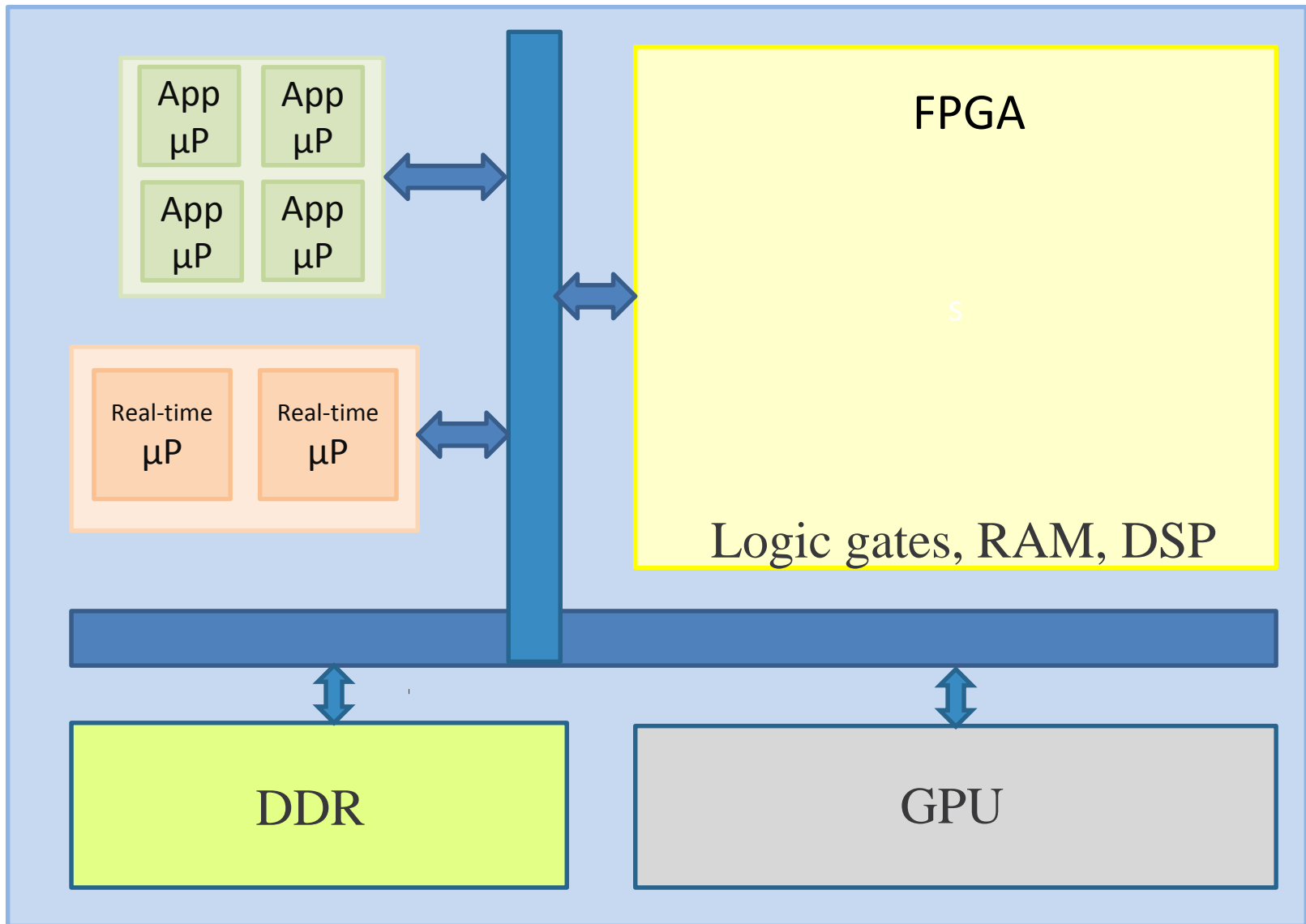Bachelor Degree 2004-2007

Work Experience 2008-2012

Master 2013-2015

Work Experiece 2015/2016

PhD Candidate UPM 2016

GOAL

Heterogeneous device (example)

App μP | App μP
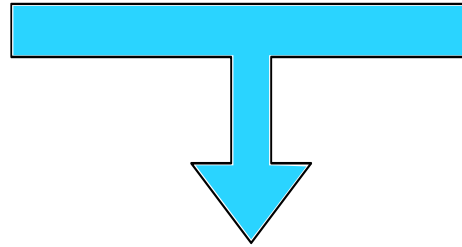App μP | App μP

Real-time μP | Real-time μP

FPGA

Logic gates, RAM, DSP

DDR

GPU

ReCoSoC Conference Paper

Rapid prototyping of software applications enabling:

- deadlock-free code generation using **PiSDF MoC**

- custom **hardware acceleration** and generation

Thank you for your attention!

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