

> Past: CAIRN Team, IRISA (PhD in June 2013)

- Compiler techniques for HLS/MPSoCs
- Also 1y in the industry: code quality



> Now: Research Engineer working on Preesm

- IDE for prototyping SDF programs
- Clean, redesign, test, maintain, support, ...



> Future: CERBERO European Project integration

- Plug Preesm with partner tools
- Previous + document, teach, diffuse, ...

